Stability and Control of Ad Hoc DC Microgrids

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Abstract-Ad hoc electrical networks are formed by connecting power sources and loads without pre-determining the network topology. These systems are well-suited to addressing the lack of electricity in rural areas because they can be assembled and modified by non-expert users without central oversight. There are two core aspects to ad hoc system design: 1) designing source and load units such that the microgrid formed from the arbitrary interconnection of many units is always stable and 2) developing control strategies to autonomously manage the microgrid (i.e., perform power dispatch and voltage regulation) in a decentralized manner and under large uncertainty. To address these challenges we apply a number of nonlinear control techniques-including Brayton-Moser potential theory and primal-dual dynamics-to obtain conditions under which an ad hoc dc microgrid will have a suitable and asymptotically stable equilibrium point. Further, we propose a new decentralized control scheme that coordinates many sources to achieve a specified power dispatch from each. A simulated comparison to previous research is included.

I. INTRODUCTION

More than one billion people do not have electricity access [1], largely because of insufficient centralized power systems in developing countries. The need for electricity in remote and rural areas and the evolving demands on the existing bulk power infrastructure have driven extensive development of microgrids in recent years. Microgrids naturally incorporate distributed renewable sources and are inherently decentralized. However, designing and installing a microgrid to electrify an off-grid community typically requires specialized planning. Electrical networks which could be formed by the ad hoc interconnection of modular power sources and loads by non-specialist users would remove barriers to energy access, allowing decentralized electricity markets to proliferate in an unprecedented manner.

Broadly, the critical challenges of ad hoc microgrids are:

- the microgrid components (power sources, loads, and lines) should be designed so that *any* network formed by connecting many units *always* has an appropriate and stable equilibrium point,
- 2) the sources should be controlled in a coordinated and decentralized manner to manage power dispatch, and
- the system should function entirely autonomously under significant uncertainty regarding the network configuration and power supply and demand.

To evaluate stability, traditional power system operators use computationally intensive simulations and empirical testing of the pre-determined network topology. By contrast, ad hoc networks present unique challenges because the network topology is not specified. Instead, analytic constraints on the individual source, load, and line units-independent from the interconnection structure-must be developed such that, when satisfied, they guarantee the existence, feasibility, and stability of an equilibrium point. This is different from previous stability analyses in power electronics, which have relied on impedance measurements, used overly simplified network models, or placed unrealistic constraints on the network configuration [2], [3]. An additional complication is the widespread and increasing use of power electronic devices, which draw constant power at their inputs to regulate their outputs. The negative incremental impedance $(\partial v/\partial i)$ of these loads has a well-documented destabilizing effect on power systems [4]. The effect of constant power loads on stability has attracted recent interest in the controls community ([5], [6], [7], [8], [9]) but has not yet been analyzed in the context of ad hoc systems.

To plan and coordinate power dispatch, traditional ac power system operators compute the optimal dispatch based on the marginal costs of the generators, and each generator realizes its assigned power output by controlling the phase angle between its output voltage and current. Ad hoc systems again present unique challenges because of the combination of the need for decentralized computation and control and the uncertainty in power supply and demand. In dc systems powered by voltage-source converters, each source realizes its assigned power output by controlling its voltage level. A set of control techniques, organized into a hierarchy, has been widely used in ac and dc microgrids to accomplish these objectives [10], and economically optimal dispatch strategies for dc microgrids have been considered [11], but again, not in the context of nonlinear ad hoc systems.

In this paper, we develop design-friendly conditions on both the total system load and the individual source, load, and line units under which microgrids composed of these units will have a suitable and stable equilibrium point. Our conditions are summarized by Eqs. (24), (29), and (32). They are more flexible and better suited to a priori device design than previous stability criteria, and rely on techniques from nonlinear control theory [12], [13], [14], [15]. We also propose a decentralized control strategy which achieves precise power sharing and voltage regulation, and include a simulated comparison to a well-known power electronics benchmark. Throughout, we discuss how previously developed mathematical structures can be related to the analysis of stability and control of ad hoc systems and highlight opportunities and challenges related to this emerging class of power systems.

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II. MODELS AND NOTATION

In this section we present models for the interconnecting lines, power electronic loads, and voltage source converters which are analytically tractable and can be adapted to describe many networks. These are summarized in Fig. 1 and are based on a previously presented ad hoc microgrid [16].

A. Network Structure

The electrical network is described as a weighted, directed graph $(\mathcal{V}, \mathcal{E})$ with a total of $|\mathcal{V}| = n$ nodes (buses) and $|\mathcal{E}| = m$ edges (lines). A power source or load is attached to each node and the models for each element are depicted in Fig. 1. The electric state of the system is described by the voltage and current vectors $v \in \mathbb{R}^n$ and $i \in \mathbb{R}^m$. The topology of the graph is defined by an incidence matrix $\nabla \in \mathbb{R}^{m \times n}$, such that applying ∇ to the voltage vector results in a potential drop across line α with source bus s_{α} and target bus t_{α} :

$$(\nabla v)_{\alpha} = v_{s_{\alpha}} - v_{t_{\alpha}} \,. \tag{1}$$

Similarly, applying ∇^{\top} to the current vector provides the total current flowing out of each node:

$$(\nabla^{\top} i)_k = \sum_{\{\alpha: \ s_{\alpha} = k\}} i_{\alpha} - \sum_{\{\alpha: \ t_{\alpha} = k\}} i_{\alpha} \,. \tag{2}$$

B. Power Lines (Edges)

Each power line is associated with a graph edge $\alpha \in \mathcal{E}$ and is characterized by an inductance L_{α} and a resistance R_{α} , as shown in Fig. 1a. Each line has time constant $\tau_{\alpha} = L_{\alpha}/R_{\alpha}$, and each i_{α} is described by:

$$L_{\alpha}\dot{i}_{\alpha} = -R_{\alpha}i_{\alpha} + (\nabla v)_{\alpha}, \quad \alpha \in \mathcal{E}.$$
 (3)

C. Sources/Loads (Nodes)

 R_{α}

Each bus has either a load or a source attached to it. We denote the subset of vertex indices corresponding to loads as $\mathcal{V}_l \subset \mathcal{V}$ with $|\mathcal{V}_l| = n_l$ and the subset of source indices as $\mathcal{V}_s \subset \mathcal{V}$ with $|\mathcal{V}_s| = n_s$. Load k is represented by the parallel connection of a capacitance C_k and a constant power

load drawing power p_k . In general, constant power loads represent perfectly-regulated power converters, and hence are conservative and general models which can be used to describe many power electronic devices. The capacitor across the input of the power converter is a standard feature of these converters, and is critical for system stability [8]. Each load voltage is described by:

$$C_k \dot{v}_k = -\frac{p_k}{v_k} - (\nabla^\top i)_k, \quad k \in \mathcal{V}_l.$$
(4)

Sources are represented as a voltage source with value u_k in series with a resistance r_k . Both u_k and r_k are internal control parameters and can be varied independently—an overview is provided in Section V and in [10], [17]. To simplify our notation, a parallel capacitor is also included in the source model. Unlike the load capacitor, this is not typically present in power converters, so in our final results we will take the limit $C_k \rightarrow 0$ for the source buses. Each source voltage is described by:

$$C_k \dot{v_k} = \frac{u_k - v_k}{r_k} - (\nabla^\top i)_k \,, \quad k \in \mathcal{V}_s \,, \tag{5}$$

which reduces to

$$v_k = u_k - r_k (\nabla^{\perp} i)_k \tag{6}$$

as $C_k \to 0$.

Finally, we introduce a simplified control method for u_k , mainly to demonstrate how the stability of source voltage control strategies can be rigorously analyzed:

$$C_u \dot{u}_k = \frac{v_{\text{ref}} - v_k}{r_k} , \quad k \in \mathcal{V}_s , \tag{7}$$

where v_{ref} is the nominal network voltage and C_u is a control parameter (chosen instead of the more conventional τ_u for dimensional convenience). This control strategy is not typically used—a coordinated strategy for practical use is presented in Section V.



(a) Line with impedance $R_{\alpha} + j\omega L_{\alpha}$. $L_{\alpha}/R_{\alpha} = \tau_{\alpha}$.

(b) Ideal model of load and associated power electronics: constant power load with parallel stabilizing capacitance C_k .

(c) A voltage source converter with droop resistance r_k and parasitic parallel capacitance $C_k \rightarrow 0$.

Fig. 1: Representations of lines, loads, and sources in our dc microgrid.

III. DYNAMICS AND STABILITY

In this section we analyze the stability of arbitrary interconnections of sources, loads, and lines, under the assumption that each source is controlled according to Eq. (7). This extends our previous, linear analysis [18] to include nonlinear constant power load models and to apply to more general networks—specifically, those with multiple τ_{α} values.

We formulate the dynamical equations in a structured way so that we can apply the classic results by Brayton and Moser regarding the stability of nonlinear circuits [12] to ad hoc networks. This representation allows us to find a Lyapunov function which certifies the asymptotic stability of an equilibrium point. In the next section, we reduce the derived linear matrix inequalities to design-friendly constraints on the total load and individual sources, loads, and lines.

A. Brayton-Moser Representation of the System

We first rewrite the equations from Section II in vector form. We introduce the diagonal resistance matrix R =diag $[R_{\alpha}] \in \mathbb{R}^{m \times m}$ for the line resistances, the diagonal capacitance matrix C = diag $[C_k] \in \mathbb{R}^{n \times n}$ for the source and load capacitances, the resistive content of the lines $\mathcal{R}_0(i)$, and the resistive co-content of the sources and loads \mathcal{G}_0 [14]:

$$\mathcal{R}_0(i) = \frac{1}{2} i^\top R i \,, \tag{8}$$

$$\mathcal{G}_0(v,u) = \sum_{k \in \mathcal{V}_l} p_k \ln v_k + \sum_{k \in \mathcal{V}_s} \frac{v_k^2 / 2 + (v_{\text{ref}} - v_k) u_k}{r_k} \,. \tag{9}$$

These definitions allow us to write the voltage and current equations as:

$$\dot{Li} = -\partial_i \mathcal{R}_0(i) + \nabla v \,, \tag{10a}$$

$$C\dot{v} = -\partial_v \mathcal{G}_0(v, u) - \nabla^\top i \,. \tag{10b}$$

$$C_u \dot{u} = \partial_u \mathcal{G}_0(v, u) \,. \tag{10c}$$

We now introduce the Brayton-Moser (BM) Potential \mathcal{P}_0 and the matrix \mathcal{Q}_0 [12], [13]:

$$\mathcal{P}_0 = \mathcal{G}_0(v) - \mathcal{R}_0(i) + i^\top \nabla v \,, \tag{11}$$

$$Q_0 = \begin{bmatrix} -L & 0\\ 0 & C \end{bmatrix}.$$
 (12)

These allow us to represent the current and voltage equations together with a single state vector $x = [i^{\top}, v^{\top}]^{\top}$ that evolves according to quasi-gradient dynamics of the form:

$$Q_0 \dot{x} = -\partial_x \mathcal{P}_0 \,. \tag{13}$$

Although this formulation reveals the structure of the underlying dynamics, it is not sufficient to certify the stability of the system, because the natural Lyapunov function candidate $\mathcal{P}_0(x)$ is neither convex nor sign definite. However,

we can use these quantities to define a closely related BM potential \mathcal{P} and resistive co-content \mathcal{G} :

$$\mathcal{P}(x,u) = \frac{\tau_{max}}{2} \begin{bmatrix} \partial_i \mathcal{P}_0 \\ \partial_v \mathcal{P}_0 \end{bmatrix}^\top \begin{bmatrix} L & 0 \\ 0 & C \end{bmatrix}^{-1} \begin{bmatrix} \partial_i \mathcal{P}_0 \\ \partial_v \mathcal{P}_0 \end{bmatrix} + \mathcal{P}_0,$$

$$= \frac{1}{2} i^\top [\tau_{max} L - LR^{-1}L] i$$

$$+ \frac{\tau_{max}}{2} i^\top C i + \mathcal{G}(v,u). \qquad (14)$$

$$\mathcal{G}(v,u) = \mathcal{R}_0 (R^{-1} \nabla v) + \mathcal{G}_0(v,u),$$

$$(v, u) = \mathcal{R}_0 \left(R^{-1} \nabla v \right) + \mathcal{G}_0(v, u) ,$$

= $\frac{1}{2} v^\top \nabla^\top R^{-1} \nabla v + \mathcal{G}_0(v, u) .$ (15)

Here τ_{max} denotes the maximal time-constant of all the lines: $\tau_{max} = \max_{\alpha}(\tau_{\alpha})$. Note that, as $C_k \to 0$, \dot{v}_k approaches the finite limit $\dot{v}_k \to \dot{u}_k - r_k \nabla^\top \dot{i}_k$ derived from the algebraic model (Eq. (6)) and $\dot{v}_k^\top C_k \dot{v}_k$ vanishes. Hence, only load voltages contribute to $\dot{v}^\top C \dot{v}$ in Eq. (14).

The matrix Q corresponding to the new BM potential \mathcal{P} can be derived by differentiating equation (14) with respect to x and using $\partial_x \mathcal{P} = -Q\dot{x}$:

$$\begin{bmatrix} \partial_i \mathcal{P} \\ \partial_v \mathcal{P} \end{bmatrix} = \tau_{max} \begin{bmatrix} -R\dot{i} + \nabla \dot{v} + L\dot{i}/\tau_{max} \\ -\nabla^\top \dot{i} - \partial_{vv}\mathcal{G}_0 \dot{v} - C\dot{v}/\tau_{max} \end{bmatrix}, \quad (16)$$

$$Q = \tau_{max} \begin{bmatrix} R - L/\tau_{max} & -\nabla \\ \nabla^{\top} & \partial_{vv} \mathcal{G}_0 + C/\tau_{max} \end{bmatrix}.$$
 (17)

Finally, in the limit $C_k \rightarrow 0$ on source nodes, the equation for u is:

$$C_u \dot{u} = \partial_u \mathcal{P} \,. \tag{18}$$

This concludes our derivation of the Brayton-Moser representation of the system dynamics in terms of the potentials \mathcal{P} and \mathcal{G} and matrix Q. These equations expose the fundamental structure of the system and will be used in the next section to analyze system stability.

B. Equilibrium Point Stability

Stability of the system can be certified by noticing that the combination of Eq. (18) and $Q\dot{x} = -\partial_x \mathcal{P}$ with $Q \succ 0$ can be interpreted as primal-dual dynamics with respect to the Lagrangian $-\mathcal{P}$ [15]. The system attempts to maximize the Lagrangian with respect to the primal variable x and minimize it with respect to the dual variable u. The dynamics converge to an equilibrium point whenever the Lagrangian is locally concave with respect to the primal variable $(\partial_{uu}\mathcal{P} \succeq 0)$, and convex with respect to the dual variable $(\partial_{uu}\mathcal{P} \preceq 0)$. The second condition is automatically satisfied because \mathcal{P} is affine in u, while the first condition is equivalent to $\partial_{vv}\mathcal{G} \succeq 0$ and can be satisfied by imposing certain constraints on the network and the operating point which are discussed below.

The asymptotic stability of the system can be formally established by considering the Lyapunov function V:

$$V = \dot{x}^{\top} \mathcal{Q} \dot{x} + C_u \dot{u}^{\top} \dot{u} \,. \tag{19}$$

If there is an equilibrium point $(x^*, u^*) = ([i^{*\top}, v^{*\top}]^{\top}, u^*)$, V exhibits non-decreasing behavior in its neighborhood:

$$\dot{V} = -\dot{x}^{\top} \left[\partial_{xx} \mathcal{P} + \dot{\mathcal{Q}} \right] \dot{x} \le 0.$$
(20)

Note that the matrix \hat{Q} vanishes at equilibrium and can be made arbitrarily small in its neighborhood, so the system is asymptotically stable whenever $\partial_{xx}\mathcal{P}(x^*, u^*) \succ 0$. Notably, these conditions can be also interpreted as the contraction behavior of the underlying dynamics [15], [19] (see also example 3.7 of [20]).

Going beyond asymptotic stability is extremely challenging. Explicit construction of the attraction regions or their approximations is beyond the scope of this work. However, a simple univariate form of nonlinearity $-p_k/v_k$ suggests that the approaches based on sector bounding of nonlinear terms, recently used for characterization of the attraction regions in ac power systems (see [21], [22]) may be appropriate for this problem as well.

In summary, existence, feasibility and stability of at least one operating point is certified as long as the following three conditions are satisfied:

- 1) There exist equilibrium voltage vectors v^*, u^* that satisfy $\partial_v \mathcal{G}(v^*, u^*) = 0$ and $\partial_u \mathcal{G}(v^*, u^*) = 0$, and all load voltages satisfy $v_k^* > v_{\min}$. The equilibrium current vector is then given by $i^* = R^{-1} \nabla v^*$.
- 2) The Hessian of the resistive co-content is positive definite at the equilibrium point: $\mathcal{H}^* = \partial_{vv} \mathcal{G}(v^*, u^*) \succ 0$.
- The matrix Q is positive semidefinite at the operating point. Our definition of τ_{max} guarantees that L − τ_{max}R ≥ 0, so positive semidefiniteness of Q reduces to C + τ_{max}∂_{vv}G₀(v^{*}, u^{*}) ≥ 0.

Whenever the three conditions above hold, the stability of an operating point follows directly from LaSalle's invariance principle.

IV. DESIGN CONSTRAINTS

The criteria derived in Section III can be applied directly to any power system with a specified network structure. However, for ad hoc networks, these criteria must be reformulated in terms of individual components—removing the dependence on ∇ —which we do in this section.

We begin with a few basic assumptions:

- 1) The network graph is strongly connected.
- 2) There is at least one source.
- The total power consumption of all the loads is bounded from above: ∑_{k∈Vl} p_k ≤ p_Σ.
- The total resistance of all lines is bounded from above: ∑_{α∈ε} R_α ≤ R_Σ.

A. Existence of the Equilibrium

The equilibrium conditions for the system (Eq. (10)) can be rewritten in a more traditional load-flow form:

$$p_k = -v_k^* (\nabla^\top R^{-1} \nabla v^*)_k \quad k \in \mathcal{V}_l \,, \tag{21a}$$

$$v_k^* = v_{\text{ref}} \quad k \in \mathcal{V}_s \,, \tag{21b}$$

$$i_{\alpha}^* = R_{\alpha}^{-1} (\nabla v^*)_{\alpha} \,. \tag{21c}$$

Sufficient conditions for the existence of a solution to these load flow equations have been recently proposed in [6] and later extended to a number of other settings in a series of follow-up works including [7], [23], [24]. It has been shown in [6] that the solution of Eq. (21) is guaranteed to exist whenever the total load is bounded by $p_{\Sigma} \leq v_{\text{ref}}^2/(4||Z||_{\infty}^*)$, where Z is the effective impedance matrix, which in our notation could be written as $Z = (\nabla_l^\top R^{-1} \nabla_l)^{-1}$ with ∇_l being the submatrix of ∇ corresponding to load buses. The expression $||Z||_{\infty}^*$ also admits a simple interpretation. Whenever a current $(\delta j)_k$ is injected at load bus k, the voltage on this bus rises by $(\delta v)_k$. Then $||Z||_{\infty}^* = \max_{k \in \mathcal{V}_l} (\delta v)_k / (\delta j)_k$ the maximal effective resistance between the load and source buses. From the assumptions stated above, the maximum resistance between two nodes cannot exceed R_{Σ} , and hence $||Z||_{\infty}^* \leq R_{\Sigma}$.

This upper bound is reached when a single generator and a single load are separated by a power line of resistance R_{Σ} , so that is the "worst case" topology with respect to that condition. We conclude that, in the presence of uncertainty regarding the network structure, the solution to Eq. (21) is guaranteed to exist if and only if the maximum network load satisfies:

$$p_{\Sigma} \le \frac{v_{\text{ref}}^2}{4R_{\Sigma}} \,. \tag{22}$$

B. Feasibility of the Equilibrium

In addition to the existence of an equilibrium point, power systems typically also have a minimum voltage level requirement such that $v_k \ge v_{\min}$ on every load bus. In practical systems, this requirement ("feasibility" of an equilibrium point) is stricter than the condition for existence of an equilibrium point. The corresponding design constraint can be derived using the expression for the voltage level:

$$v_{k} = v_{\text{ref}} - \sum_{l \in \mathcal{V}_{l}} Z_{kl} \frac{p_{l}}{v_{l}},$$

$$\geq v_{\text{ref}} - \frac{1}{\min_{l \in \mathcal{V}_{l}} v_{l}} \sum_{l \in \mathcal{V}_{s}} |Z_{kl}| p_{l},$$

$$\geq v_{\text{ref}} - \frac{1}{\min_{l \in \mathcal{V}_{l}} v_{l}} ||Z||_{\infty}^{*} p_{\Sigma}.$$
(23)

Hence, we obtain a quadratic inequality for $\min_{k \in \mathcal{V}_l} v_k$. As long as the upper voltage solution to the load flow equations exists, it will be feasible when the following condition is satisfied:

$$p_{\Sigma} \le \frac{v_{\min}(v_{\mathrm{ref}} - v_{\min})}{R_{\Sigma}}.$$
(24)

This condition is more restrictive than (22) as long as $v_{\rm min} > v_{\rm ref}/2$. Inequality (24) becomes binding for the same "worst case" scenario as described above.

C. Convexity of the BM Potential

Next, we consider the condition for positive-definiteness of the Hessian of the BM potential at the equilibrium point: $\mathcal{H}^* = \partial_{vv} \mathcal{P}(v^*).$

We introduce a path-decomposition of the graph characterized by the matrix $\sigma \in \mathbb{R}^{n \times l}$ with elements $\sigma_{k\alpha} \in \{0, 1, -1\}$. The values of $\sigma_{k\alpha}$ are chosen to define the path from some source bus κ to bus k. Then, the voltage on bus k can be represented as:

$$v_k = v_\kappa + \sum_{\alpha=1}^m \sigma_{k\alpha} (\nabla v)_\alpha \,. \tag{25}$$

For convenience, we define another node in the original graph and assign it index k = 0. This "virtual node" is placed between the voltage source (u_{κ}) and droop resistance (r_{κ}) and creates an extra edge with index $\alpha = 0$ with edge impedance $R_0 = r_{\kappa}$. This edge connects the virtual node $s_0 = 0$ to the pre-existing node $t_0 = \kappa$. Assuming that the corresponding incidence and path-decomposition matrices are denoted as $\hat{\nabla}$ and $\hat{\sigma}$ we can write $v_{\kappa} = \hat{\sigma}_{\kappa 0}(\hat{\nabla}v)_{\kappa}$ and (25) can be rewritten as $v = \hat{\sigma}\hat{\nabla}v$, or

$$v_k = (\hat{\sigma}\hat{\nabla}v)_k = \sum_{\alpha=0}^m \sigma_{k\alpha} (\nabla v)_\alpha \,. \tag{26}$$

The quadratic form corresponding to \mathcal{H}^* acting on an arbitrary vector $v \in \mathbb{R}^n$ can be written:

$$v^{\top} \mathcal{H}^{*} v = \sum_{\alpha=1}^{m} R_{\alpha}^{-1} (\nabla v)_{\alpha}^{2} + \sum_{k \in \mathcal{V}_{s}} \frac{v_{k}^{2}}{r_{k}} - \sum_{k \in \mathcal{V}_{l}} \frac{p_{k} v_{k}^{2}}{(v_{k}^{*})^{2}}$$
$$= \sum_{\alpha=0}^{m} R_{\alpha} i_{\alpha}^{2} + \sum_{\substack{k \in \mathcal{V}_{s} \\ k \neq \kappa}} \frac{v_{k}^{2}}{r_{k}} - \sum_{\substack{k \in \mathcal{V}_{l}}} g_{k} v_{k}^{2}$$
(27)

We have introduced a current $i_{\alpha} = R_{\alpha}^{-1}(\hat{\nabla}v)_{\alpha}$ and an effective load conductance $g_k = p_k/(v_k^*)^2$ that satisfies $g_k \leq p_k/v_{\min}^2$, so the total load conductance satisfies $\sum g_k \leq p_{\Sigma}/v_{\min}^2$. These observations allow us to bound the last term of (27) with the help of the Jensen's inequality:

$$\sum_{k \in \mathcal{V}_{l}} g_{k} v_{k}^{2} = \sum_{k \in \mathcal{V}_{l}} g_{k} \left(\sum_{\alpha=0}^{m} \hat{\sigma}_{k\alpha} R_{\alpha} i_{\alpha} \right)^{2}$$

$$\leq \left(\sum_{\alpha=0}^{m} R_{\alpha} \right) \sum_{\alpha=0}^{m} R_{\alpha} i_{\alpha}^{2} \left(\sum_{k \in \mathcal{V}_{l}} \sigma_{k\alpha}^{2} g_{k} \right)$$

$$\leq (R_{\Sigma} + r_{\kappa}) \frac{p_{\Sigma}}{v_{\min}^{2}} \sum_{\alpha=0}^{m} R_{\alpha} i_{\alpha}^{2}. \tag{28}$$

Therefore, the quadratic form in (27) is non-negative whenever the total load power and total network resistance satisfy a constraint on the maximum power demand:

$$p_{\Sigma} \le \frac{v_{\min}^2}{R_{\Sigma} + r_{\kappa}} \,. \tag{29}$$

Again, the "worst case" network configuration for this condition is the maximally separated single source and load. This condition is typically less restrictive than (24), however it can be binding in microgrids that rely on large values of r_k (larger r_k values are often used for coordinated microgrid control—see Section V).

D. Asymptotic Stability of the Equilibrium

The condition $\mathcal{Q}(x^*) \succ 0$, with \mathcal{Q} defined in Eq. (17), can be rewritten as:

$$\tau_{max}/r_k > 0, \quad \forall k \in \mathcal{V}_s,$$
 (30)

$$C_k - \frac{\tau p_k}{(v_k^*)^2} > 0, \quad \forall k \in \mathcal{V}_l.$$
(31)

This is always satisfied for sources. It is satisfied for loads as long as the equilibrium voltage is bounded from below by $v_k^* > v_{\min}$ and the total power consumption of the load is bounded from above:

$$p_k < C_k \frac{v_{\min}^2}{\tau_{max}}, \quad \forall k \in \mathcal{V}_l.$$
 (32)

As a result, there is a trade-off between load power draw and required input capacitance. In practice, p_k is bounded from above by converter device ratings, so C_k can be sized according to the maximum converter capacity.

In summary, we have derived three simple criteria ((24), (29), and (32)) which can guarantee that an equilibrium point will exist, be feasible, and be asymptotically stable for any network structure. Together, these three constraints define the set of necessary and sufficient conditions under which the arbitrary interconnection of sources, loads, and lines is guaranteed to be stable—*regardless of the network topology*.

V. DC MICROGRID CONTROL

A. Control Objectives

The conditions derived in Sections III and IV ensure that a feasible operating point exists for all admissible network topologies and loading levels. In addition to stable operation, there are two more objectives that are critical for allowing multiple sources to cooperate when connected to the same microgrid:

- 1) each source must be able to set and update its fraction of total supplied power ("dynamic load sharing"), and
- 2) node voltages v_k must be maintained near the nominal network voltage v_{ref} .

When multiple sources are connected without coordinated control, the steady-state fraction of total power each supplies is fixed and is determined by the line resistances. This is not ideal—being able to control the relative power output of each source is a basic prerequisite for more advanced functionality, like economically-optimal power dispatch.

We assume that a vector of optimal power sharing proportions λ has been computed from some arbitrary set of cost functions and analyze how to realize the desired power dispatch. With λ_k defined as the participation factor that source k supplies, relative to the average, the power sharing objective is given by:

$$P_{k} = \lambda_{k} \overline{P} \equiv \frac{\lambda_{k}}{n_{s}} \sum_{l \in \mathcal{V}_{s}} P_{l}, \quad \forall k \in \mathcal{V}_{s} .$$
(33)

At the same time, the network voltage must be regulated. To quantify this objective, we define $\overline{v} = \sum_{k \in \mathcal{V}_s} v_k/n_s$ as the average of the source node voltages. Our objective is to maintain this average at the desired network voltage v_{ref} :

$$\overline{v} = v_{\rm ref} \,. \tag{34}$$

Note that: 1) if the loads also have communication capabilities they could also be included in \overline{v} , and 2) much of the microgrid control literature (including [10]) considers only two-source, one-load networks when formulating their control strategies and states their objective in terms of the single load voltage: $v_l = v_{ref}$. Where applicable, we have replaced v_l with the more general \overline{v} .

B. Hierarchical Microgrid Control

To manage complexity, microgrid control is typically separated into a hierarchy of strategies operating on different timescales and with different objectives. The standard strategy [10], which is well-known and widely-cited, is summarized below, and consists of:

- 1) Individual (uncoordinated) control of each power converter, which, for voltage-source converters, realizes the output voltage v_k .
- 2) Droop control, which modifies v_k proportionally to the output current, mimicking a resistor. The goal of droop control is achieving power sharing.
- 3) Secondary voltage control, which increases u_k to offset the voltage drop caused by droop control. In its standard form, secondary voltage control only regulates the network voltage. In our proposed control strategy, it also improves power sharing.
- 4) Tertiary control, which interfaces the microgrid with another power system. Tertiary control is outside the scope of this work.

1) Primary Control-Droop: When sources and loads are connected without coordinated control, the amount of power each source supplies is determined by the network configuration and line resistances. To modify the power sharing of the sources, a "virtual resistance" r_k is inserted at the output of each converter. r_k is a control parameter internal to the power electronic device and hence does not dissipate power. For optimal performance, r_k must be much greater than the line resistances so that the impact of the line resistance on the power sharing is negligible. In practice, $r_k \gg R_\alpha$ is typically not obtainable because larger r_k values increase the node voltage deviation: $v_k = u_k - r_k (\nabla^\top i)_k$ (see Fig. 1c as $C_k \rightarrow 0$). This means that droop control alone cannot achieve arbitrary power sharing proportions, but it remains a popular control strategy because it is simple and does not require communication if all r_k values are fixed (however, to update the power sharing proportions, the r_k values must also be updated).

2) Secondary Control—Standard Method: To mitigate the voltage deviations caused by droop control, proportional-integral control can be used to restore the network voltage level. The standard way of achieving this is by increasing u_k until equation (34) is satisfied:

$$u_k(t) = v_{\text{ref}} + k_p(v_{\text{ref}} - \overline{v}) + k_i \int_0^t (v_{\text{ref}} - \overline{v}) dt \,.$$
(35)

In this way, all the u_k values change in response to a global error signal, and under the usual assumption that $u_k(0) = v_{\text{ref}}$ for all sources, all u_k values are identical and the secondary voltage control does not affect power sharing.

3) Secondary Control—Multipurpose Method: For reasons mentioned in Sec. V-B.1, droop control alone is not sufficient to achieve the power sharing objective (Eq. (33)). Here, we propose a new method of secondary voltage control that ensures accurate power sharing in addition to network voltage regulation. We accomplish this by varying the u_k values independently, rather than enforcing equal u_k values across all sources, and combining the two control objectives into a single integral controller:

$$\dot{u}_k = k_v (v_{\text{ref}} - \overline{v}) + k_\lambda (\lambda_k \overline{P} - P_k), \qquad (36)$$

with $k_v, k_\lambda > 0$.

This is a simple controller that adjusts u_k in response to the observed power and voltage levels. It is based on the observation that increasing u_k increases both the node voltage v_k and the power output of the kth source.

The conditions for stability of this method are still unknown; the scheme does not possess the primal-dual structure that was exploited to directly certify the stability of the uncoordinated sources described by (7). However, the contraction interpretation of the Lyapunov function (19) is potentially generalizable to more sophisticated controls like (36) with proper adjustment of the contraction metric. In particular, the extension of the original contraction analysis to singular systems, developed in [25], [26], provides a natural framework for estimating the stability constraints on the controller gains k_v and k_{λ} .

C. Secondary Control: Communication Requirements

The required communication speed and volume is an important metric when comparing microgrid control strategies. Both secondary voltage methods require all sources to share their voltage values to compute \overline{v} . In addition, the proposed strategy requires communication of the source currents. Accordingly, the proposed strategy requires messages which are approximately twice as long (voltage *and* current of all sources) but no additional bandwidth. Further nonidealities, including communication bandwidth and delays, are not analyzed here.

VI. MICROGRID CONTROL SIMULATION

To compare our proposed secondary voltage strategy to the standard strategy, in this section we present simulation results obtained using the network configuration shown in Fig. 3 and the parameters given in Table I. We have chosen $k_p = 0$ to simplify the comparison because we are interested in the steady-state performance. We have also done preliminary experimental validation of the method in [18].



(a) Droop control and standard secondary control [10].

(b) Droop control and multipurpose secondary control.

Fig. 2: Our proposed power sharing strategy compared to the standard method of hierarchical microgrid control. Parameters chosen to achieve equal power sharing. The proposed strategy eliminates steady-state error by allowing each u_k to vary independently.



Fig. 3: Visualization of the simulated network. Sources are depicted as squares, and loads as circles. Grey units turned on at 10 ms—all other units were on throughout.

TABLE I: Simulation Parameters

Parameter	Description	Value
$v_{\rm ref}$	Nominal Network Voltage	$48\mathrm{V}$
R_{lpha}	Line Resistance	0.111Ω
$ au_{lpha}$	Line Time Constant	$55.45\mu s$
r_k	Droop Resistance	0.5Ω
C	Load Input Capacitance	$845.7\mathrm{pH}$
p_k	Load Power	$35.11\mathrm{W}$
k_p	Proportional Gain	0
k_i	Integral Gain	$18.02{ m s}^{-1}$
k_v	Voltage Error Gain	$36.04{ m s}^{-1}$
k_{λ}	Power Sharing Error Gain	$0.7508 \mathrm{A^{-1} s^{-1}}$
λ_k	Participation Factor of Source k	1

The network has a total of seven loads—initially, two were on, and the other five turned on at 10 ms as shown in Fig. 4. Source, load, and line models are as depicted in Fig. 1. The power sharing results obtained by the standard voltage restoration strategy and our strategy are shown in Fig. 2. The droop resistances and λ_k values were designed for all sources to share equally ($P_1 = P_2 = P_3$). The proposed strategy obtains precise power sharing because it uses the secondary voltage control for power sharing in addition to voltage regulation. The standard strategy has an unavoidable steady-state power sharing error because it uses only proportional primary



Fig. 4: Total power drawn by all loads in the network.



Fig. 5: Voltage regulation capability of the proposed strategy.

(droop) control. Both approaches achieve comparable voltage regulation (results for the standard strategy are not shown), and the source node voltages in the proposed strategy are shown in Fig. 5.

VII. CONCLUSIONS AND PATH FORWARD

Ad hoc microgrids present an alternative to the inherently centralized nature of bulk power systems. By allowing nonspecialist consumers to safely configure and maintain their own electricity infrastructure and markets, they have unparalleled potential to provide power to communities underserved by traditional power systems. In the past, development of ad hoc networks has been impeded by the difficulty of guaranteeing the stability of arbitrary interconnections of power electronic subsystems a priori. In this paper we have relied on a number of classical control theory techniques, including Brayton-Moser potentials, primal-dual dynamics, and recent approaches to the analysis of load flow equations, to develop conditions under which ad hoc dc microgrids are stable. In particular, we have developed conditions on the overall system power consumption (Eqs. (24) and (29)) and individual load capacitance values (Eq. (32)) such that the stability of arbitrary interconnections of voltage-source converters and tightly-regulated power electronic loads is guaranteed.

In addition, we have proposed a new decentralized control strategy for autonomous coordination of many sources to achieve dynamic load sharing while regulating the network voltage. As shown in our simulated comparison, this strategy obtains more accurate power sharing than the decentralized microgrid control techniques used in practice.

Finally, there are several exciting open questions that still need to be answered:

- What are admissible and optimal values of the decentralized control gains that ensure system stability and performance in ad hoc microgrids?
- Can more flexible stability conditions be derived by adding carefully chosen constraints to the network topology—for example, by focusing on a particular class of networks?
- What are the effects of practical limitations (like communication delays and outages) on decentralized controller performance?
- How can the transient stability of ad hoc systems be characterized?
- How resilient are ad hoc systems to disturbances caused by routine switching events and/or emergency faults?

In this paper, we have addressed some challenges inherent to ad hoc microgrids and hope that these challenges continue to be resolved, with the ultimate goal of enabling the largescale deployment of truly decentralized power systems.

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